

## **Zhijian Li**

Professor Li (1928.5-2011.5) was born in Ningbo, Zhejiang province. He obtained his bachelor's degree in physics from Zhejiang University in 1951 and was conferred his Ph.D. in Sciences by the National University of Leningrad of the former Soviet Union. He had held the position of director of the Institute of Microelectronics, Tsinghua University for many years. He was member of Expert Advisory Committee of National Construction of Information Society and awarded as honorable member of Chinese Institute of Electronics.

Professor Li began semiconductor research in 1953. He, then, successfully proposed an intercrystalline electron barrier model explaining the photo conduction behaviors of thin semiconductor films. He made significant contributions to the early development of silicon-based technology in China. Professor Li began his research on ICs in 1980's. In the 80's, the research group under his leadership succeeded in developing a series of VLSI chips, such as the 2 Kbit EEPROM, the 16Kbit SRAM and the 16bit CPU. Since 1990, Professor Li's research interest was focused on the "System-on-a-chip" integration technology.

Professor Li had also gained very positive results in the development of silicon based MEMS technology. He had received National Prizes for many times. He was awarded the "1997 Tan Kahkee Prize for Information Science" and the "2000 Ho Lee Award of Scientific and Technological Progress".